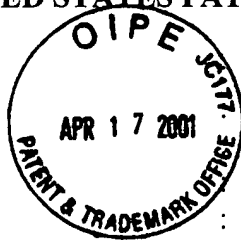


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tetsushi TANIZAKI, et al.



Serial No.: 09/712,246

Group Art Unit: 2818

Filed: November 15, 2000

Examiner:

For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT  
OPERATING AT HIGH RATE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference is accompanied by an English abstract.

Respectfully submitted,

MODERMOTT, WILL & EMERY

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